

# ADE4INLANG

## 4 Port Intel Gigabit LAN Jetway Daughter Board Gen2



### Features

Easily add four additional Intel gigabit LAN ports.

Compatible with selected Jetway motherboards that support the new Jetway Daughter Board Gen2 interface,.

1. (4) four Intel 82574L PCI-E gigabit ethernet controllers
2. (4) four activity LED headers
3. Full Duplex flow control (IEEE 802.3x)
4. Wake-on-LAN and remote wake-up support
5. Fully compliant with IEEE 802.3, IEEE 802.3u, IEEE 802.3ab
6. Proprietary Jetway Daughter Board Gen2 interface (PCI Express bus)

### Specifications

<b>Model</b>	■ ADE4INLANG
<b>Part No.</b>	■ JADE4INLANG
<b>Dimension</b>	■ 75 x 82 mm
<b>Compatible With</b>	■ JNF9G-QM77, JNF9I-2550
<b>Chipset</b>	■ Intel 82574L PCI Express Gigabit Ethernet Controller
<b>Interface</b>	■ Proprietary Jetway Daughter Board Gen2
<b>Features</b>	<ul style="list-style-type: none"><li>■ Programmable host memory receive buffers (256 bytes to 16 KB)</li><li>■ Intelligent interrupt generation features to enhance driver performance</li><li>■ Message Signaled Interrupts (MSI and MSI-X)</li><li>■ Configurable receive and transmit data FIFO, programmable in 1KB increments</li><li>■ Fully compliant with IEEE 802.3, IEEE 802.3u, IEEE 802.3ab</li><li>■ IEEE 802.3ab auto negotiation support, Auto MDI, MDI-X crossover at all speeds</li><li>■ Full duplex operation at 10/100/1000 Mb/s, Half duplex at 10/100 Mb/s</li><li>■ TCP segmentation capability compatible with Large Send offloading features</li><li>■ Support up to 256KB TCP segmentation (TSOv2)</li><li>■ Fragmented UDP checksum offload for packet reassemble</li><li>■ IPv4 and IPv6 checksum offload support (receive, transmit, and large send)</li><li>■ Receive Side Scaling (RSS) with two hardware receive queues</li><li>■ 9KB jumbo frame support, 40KB packet buffer size, NC-SI for remote management core</li><li>■ Magic Packet* wake-up enable with unique MAC address</li><li>■ ACPI register set and power down functionality supporting D0 and D3 states</li><li>■ Full wake up support (APM and ACPI 2.0), Smart power down at S0 and Sx no link</li></ul>